

August 1998

DESCRIPTION

- **Ultralow Quiescent Current: 3.5 μ A Typ**
- **Open-Drain Outputs Typically Sink >20mA**
- **Wide Supply Range:**
 - Single: 2.5V to 11V
 - Dual: ± 1.25 V to ± 5.5 V
- **Input Voltage Range Includes the Negative Supply**
- **Reference Output Drives 0.01 μ F Capacitor**
- **Adjustable Hysteresis**
- **12 μ s Propagation Delay with 10mV Overdrive**
- **No Crowbar Current**
- **Pin Compatible Upgrade for MAX982**

- Battery-Powered System Monitoring
- Threshold Detectors
- Window Comparators
- Oscillator Circuits

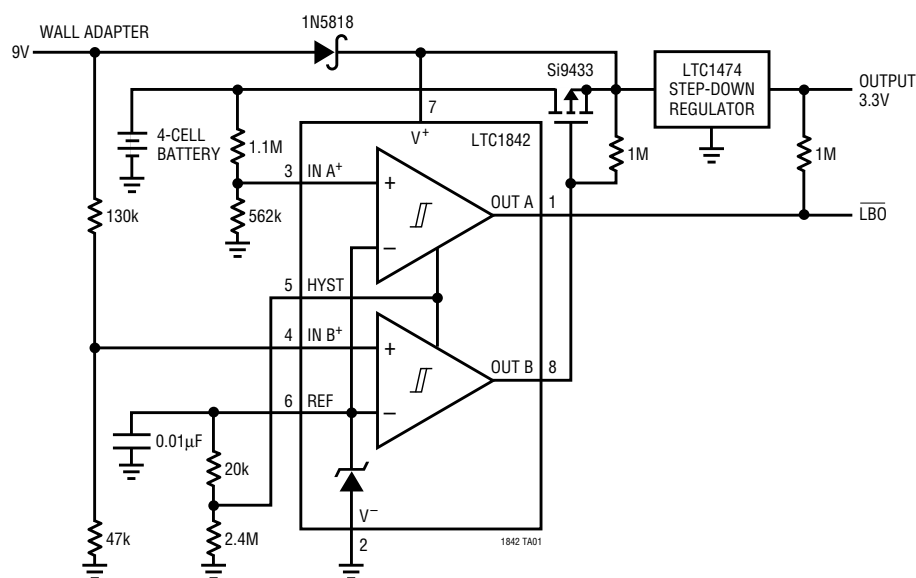
The LTC®1842 is an ultralow power dual comparator with built-in reference. The comparator features less than 5.7µA supply current over temperature, a 1.182V ±1% reference, programmable hysteresis and open-drain outputs that sink current. The reference output can drive a bypass capacitor of up to 0.01µF without oscillation.

The comparator operates from a single 2.5V to 11V supply or a dual $\pm 1.25\text{V}$ to $\pm 5.5\text{V}$ supply. Comparator hysteresis is easily programmed by using two resistors and the HYST pin. The comparator's input operates from the negative supply to within 1.3V of the positive supply. The comparator output stage can typically sink greater than 20mA. By eliminating the cross-conduction current that normally happens when the comparator changes logic states, power supply glitches are eliminated.

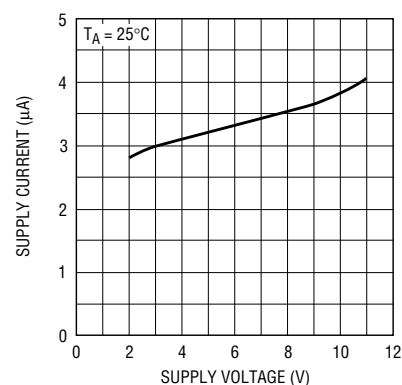
The LTC1842 is available in 8-pin PDIP and SO packages.

 LTC and LT are registered trademarks of Linear Technology Corporation.

Battery Switchover Circuit



LTC1842 Supply Current vs Supply Voltage



ABSOLUTE MAXIMUM RATINGS

(Note 1)

Voltage

 V^+ to V^- 12V to $-0.3V$ IN^+ , IN^- , HYST ($V^+ + 0.3V$) to ($V^- - 0.3V$)REF ($V^+ + 0.3V$) to ($V^- - 0.3V$)OUT 12V to ($V^- - 0.3V$)

Current

 IN^+ , IN^- , HYST 20mA

REF 20mA

OUT 50mA

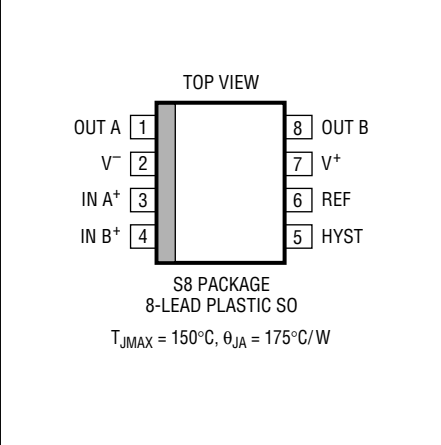
OUT Short-Circuit Duration ($V^+ \leq 5.5V$) Continuous

Power Dissipation 500mW

Operating Temperature Range

LTC1842C $0^\circ C$ to $70^\circ C$ LTC1842I $-40^\circ C$ to $85^\circ C$ Storage Temperature Range $-65^\circ C$ to $150^\circ C$ Lead Temperature (Soldering, 10 sec) $300^\circ C$

PACKAGE/ORDER INFORMATION

	ORDER PART NUMBER
	LTC1842CS8 LTC1842IS8
	S8 PART MARKING
	1842 1842I

Consult factory for Military grade parts.

ELECTRICAL CHARACTERISTICS $V^+ = 5V$, $V^- = 0V$, $T_A = 25^\circ C$ unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS			MIN	TYP	MAX	UNITS
Power Supply								
V ⁺	Supply Voltage Range			●	2.5		11	V
I _{CC}	Supply Current	HYST = REF (Note 2)		●		3.5	5.7	μA
Comparator								
V _{OS}	Comparator Input Offset Voltage	V _{CM} = 2.5V		●		±3	±10	mV
I _{IN}	Input Leakage Current (IN ⁺ , IN [−])	IN ⁺ = IN [−] = 2.5V		●		±0.01	±1.0	nA
V _{IN}	Comparator Input Voltage Range			●	V [−]		V ⁺ − 1.3V	V
PSRR	Power Supply Rejection Ratio	V ⁺ = 2.5V to 11V				0.1	1	mV/V
NOISE	Voltage Noise	100Hz to 100kHz				100		μV _{RMS}
V _{HYST}	Hysteresis Input Voltage Range			●	V _{REF} − 50mV		V _{REF}	V
t _{PD}	Propagation Delay	C _{OUT} = 10pF, R _{PULL-UP} = 100k	Overdrive = 10mV Overdrive = 100mV			12 4		μs μs
I _{LEAK}	Output Leakage Current	V _{OUT} = 12V (Note 2)		●		1	100	nA
V _{OL}	Output Low Voltage	I _{OUT} = 1.8mA		●			V [−] + 0.4V	V
Reference								
V _{REF}	Reference Voltage	No Load	25°C		1.174	1.182	1.190	V
			C Temp Range	●	1.170		1.194	V
			I Temp Range	●	1.164		1.200	V
ΔV _{REF}	Load Regulation	I _{SOURCE} = 1mA		●		1	3	mV
		I _{SINK} =10μA		●		1.75	4 6	mV mV
e _n	Voltage Noise	100Hz to 100kHz				100		μV _{RMS}

ELECTRICAL CHARACTERISTICS

$V^+ = 3V$, $V^- = 0V$, $T_A = 25^\circ C$ unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS			MIN	TYP	MAX	UNITS
Power Supply								
V ⁺	Supply Voltage Range			●	2.5		11	V
I _{CC}	Supply Current	HYST = REF (Note 2)		●		3.5	5.5	μA
Comparator								
V _{OS}	Comparator Input Offset Voltage	V _{CM} = 1.5V		●		±3	±10	mV
I _{IN}	Input Leakage Current (IN ⁺ , IN [−])	IN ⁺ = IN [−] = 1.5V		●		±0.01	±1	nA
V _{IN}	Comparator Input Voltage Range			●	V [−]		V ⁺ − 1.3V	V
PSRR	Power Supply Rejection Ratio	V ⁺ = 2.5V to 11V				0.1	1	mV/V
NOISE	Voltage Noise	100Hz to 100kHz				100		μV _{RMS}
V _{HYST}	Hysteresis Input Voltage Range			●	V _{REF} − 50mV		V _{REF}	V
t _{PD}	Propagation Delay	C _{OUT} = 10pF, R _{PULL-UP} = 100k	Overdrive = 10mV Overdrive = 100mV			14 5		μs μs
I _{LEAK}	Output Leakage Current	V _{OUT} = 12V (Note 2)		●		1	100	nA
V _{OL}	Output Low Voltage	I _O = 0.8mA		●			V [−] + 0.4V	V
Reference								
V _{REF}	Reference Voltage	No Load	25°C		1.174	1.182	1.190	V
			C Temp Range	●	1.170		1.194	V
			I Temp Range	●	1.164		1.200	V
ΔV _{REF}	Load Regulation	I _{SOURCE} = 1mA		●		2	6	mV
		I _{SINK} =10μA		●		1.75	4 6	mV mV
e _n	Voltage Noise	100Hz to 100kHz				100		μV _{RMS}

The ● denotes specifications which apply over the full operating temperature range.

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: $IN^+ = IN^- + 80mV$, output is in high impedance state.

PIN FUNCTIONS

OUT A (Pin 1): Comparator A Open-Drain Output. Output can typically sink $>20mA$.

V^- (Pin 2): Negative Supply.

$IN A^+$ (Pin 3): Noninverting Input of Comparator A. Input common mode range extends from V^- to $V^+ - 1.3V$. Input current is typically 10pA at $25^\circ C$.

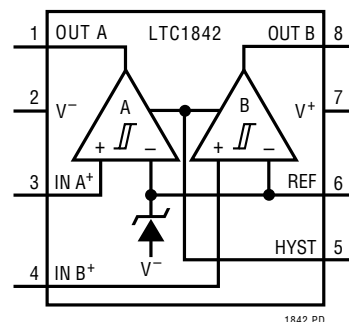
$IN B^+$ (Pin 4): Noninverting Input of Comparator B. Input common mode range extends from V^- to $V^+ - 1.3V$. Input current is typically 10pA at $25^\circ C$.

HYST (Pin 5): Hysteresis Input. Connect to REF if not used. Input voltage range is from V_{REF} to $V_{REF} - 50mV$.

REF (Pin 6): Reference Output. 1.182V with respect to V^- . Can typically source $>1mA$ and sink $10\mu A$ at $25^\circ C$. Can drive $0.01\mu F$ bypass capacitor without oscillation.

V^+ (Pin 7): Positive Supply. 2.5V to 11V.

OUT B (Pin 8): Comparator B Open-Drain Output. Output can typically sink $>20mA$.



APPLICATIONS INFORMATION

The LTC1842 is a dual micropower comparator with a built-in 1.182V reference. Features include programmable hysteresis, wide supply voltage range (2.5V to 11V) and the ability for the reference to drive up to a 0.01 μ F capacitor without oscillation. The comparator's open-drain outputs can typically sink greater than 20mA and the supply current glitches that normally occur when switching logic states have been eliminated.

Power Supplies

The comparator operates from a single 2.5V to 11V or dual ± 1.25 V to ± 5.5 V supply. If the reference output is required to source more than 1mA or the supply source impedance is high, V^+ should be bypassed with a 0.1 μ F capacitor.

Comparator Inputs

The comparator input can swing from the negative supply V^- to within 1.3V (max) of the positive supply V^+ . The input can be forced 300mV below V^- or above V^+ without damage and the typical input leakage current is only ± 10 pA.

Comparator Outputs

Each comparator output is an open-drain pull-down to V^- typically capable of sinking greater than 20mA. The low output leakage current while in three-state mode allows a high value pull-up resistor to be used. The open-drain outputs can be wire OR-ed or used in level shifting applications.

Voltage Reference

The internal bandgap reference has an output voltage of 1.182V referenced to V^- . The reference accuracy is 1.5% from -40°C to 85°C . It can typically source greater than

1mA and sink up to 10 μ A with a 5V supply. The reference can drive a bypass capacitor of up to 0.01 μ F without oscillation. By inserting a series resistor, capacitance values up to 100 μ F can be used (Figure 1).

Figure 2 shows the resistor value required for different capacitor values to achieve critical damping. Bypassing the reference can help prevent false tripping of the comparators by preventing glitches on V^+ or reference load transients from disturbing the reference output voltage.

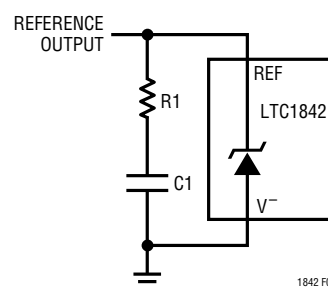


Figure 1. Damping the Reference Output

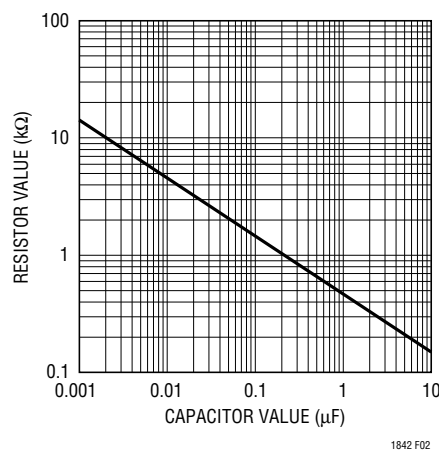


Figure 2. Damping Resistance vs Bypass Capacitor Value

APPLICATIONS INFORMATION

Figure 3 shows the bypassed reference output with a square wave applied to the V^+ pin. Resistors R2 and R3 set a 10mV hysteresis voltage band while R1 damps the reference response. Note that the comparator output doesn't trip.

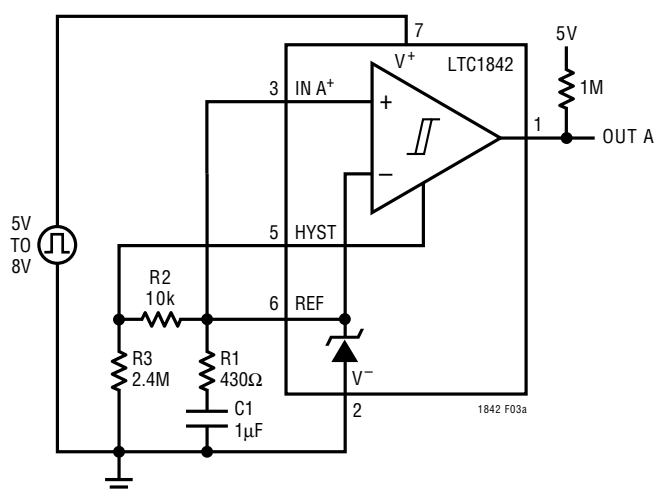


Figure 3a. Power Supply Transient Test Circuit

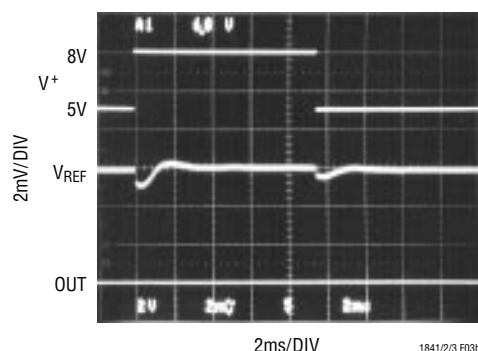


Figure 3b. Power Supply Transient Rejection

Hysteresis

Hysteresis can be added to the LTC1842 by connecting a resistor (R1) between the REF and HYST pins and a second resistor (R2) from HYST to V^- (Figure 4).

The difference between the upper and lower threshold voltages, or hysteresis voltage band (V_{HB}), is equal to twice the voltage difference between the REF and HYST pins.

As more hysteresis is added, the upper threshold increases the same amount as the low threshold decreases. The maximum voltage allowed between REF and HYST pins is 50mV, producing a maximum hysteresis voltage band of 100mV. The hysteresis band may vary by up to 15%. If hysteresis is not wanted, the HYST pin should be shorted to REF. Acceptable values for I_{REF} range are from 0.1μA to 5μA. If 2.4M is chosen for R2, then the value of R1 is equal to the value of V_{HB} .

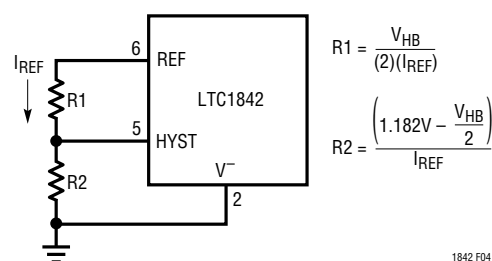


Figure 4. Programmable Hysteresis

Dual Level Detector

The LTC1842 is ideal for use as a micropower dual level detector as shown in Figure 5. The values of R1, R2 and R3 are selected for a 4.5V lower threshold and a 5.5V upper threshold. R4 and R5 set the hysteresis voltage.

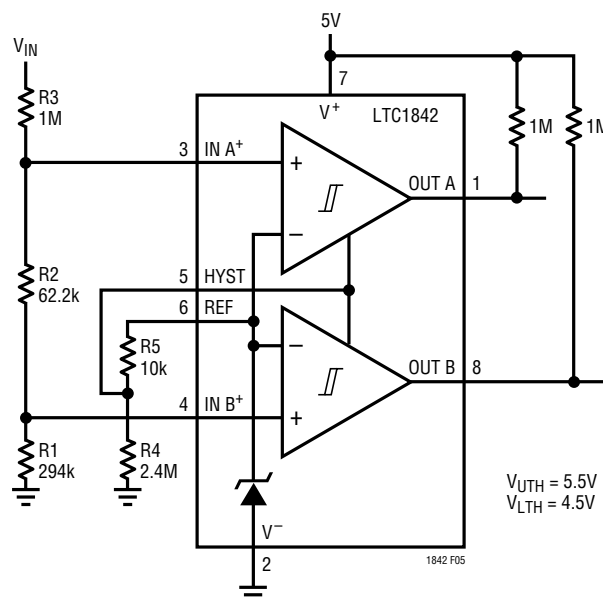


Figure 5. Dual Level Detector

APPLICATIONS INFORMATION

The following design procedure can be used to select the component values:

1. Choose the required hysteresis voltage band and calculate values for R4 and R5 according to the formulas in the hysteresis section. In this example, $\pm 5\text{mV}$ of hysteresis has been added at the comparator input ($V_H = V_{HB}/2$). Note that the hysteresis apparent at V_{IN} will be larger because of the input resistor divider.
2. Select R1. The leakage current into $IN\ B^-$ is under 1nA so the current through R1 should exceed 100nA , to ensure threshold accuracy. R1 values up to about 10M can be used, but values in the 100k to 1M range are usually easier to deal with. In this example choose $R1 = 294\text{k}$.
3. Calculate $R2 + R3$. The upper threshold should be set at 5.5V . The design equation is as follows:

$$\begin{aligned} R2 + R3 &= R1 \left(\frac{V_{UTH}}{V_{REF} + V_H} - 1 \right) \\ &= 294\text{k} \left(\frac{5.5}{1.182 + 0.005} - 1 \right) \\ &= 1.068\text{M} \end{aligned}$$

4. Calculate R2. The lower threshold should be set at 4.5V . The design equation is as follows:

$$\begin{aligned} R2 &= (R1 + R2 + R3) \frac{V_{REF} - V_H}{V_{LTH}} - R1 \\ &= (294\text{k} + 1.068\text{M}) \frac{1.182 - 0.005}{4.5} - 294\text{k} \\ &= 62.2\text{k} \end{aligned}$$

Choose $R2 = 61.9\text{k}$ (1% standard value)

5. Calculate R3:

$$\begin{aligned} R3 &= (R2 + R3) - R2 \\ &= 1.068\text{M} - 61.9\text{k} \\ &= 1.006\text{M} \end{aligned}$$

Choose $R3 = 1\text{M}$ (1% standard value)

6. Verify the resistor values. The equations are as follows, evaluated for the above example:

Upper threshold:

$$\begin{aligned} V_{UTH} &= (V_{REF} + V_H) \frac{R1 + R2 + R3}{R1} \\ &= 5.474\text{V} \end{aligned}$$

Lower threshold:

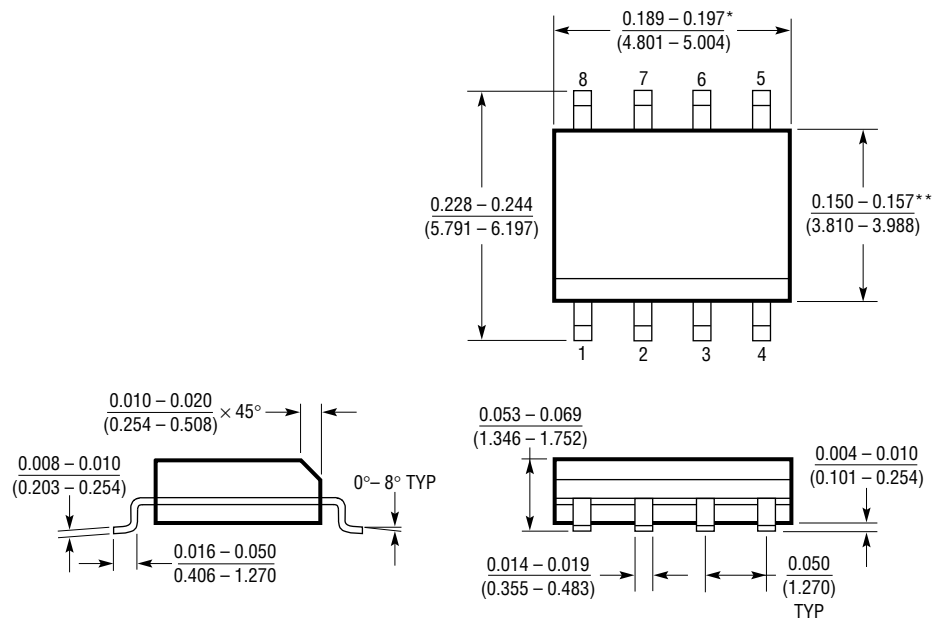
$$\begin{aligned} V_{LTH} &= (V_{REF} - V_H) \frac{R1 + R2 + R3}{R1 + R2} \\ &= 4.484\text{V} \end{aligned}$$

where the hysteresis voltage $V_H = (V_{REF}) \left(\frac{R5}{R4} \right)$

PACKAGE DESCRIPTION

Dimensions in inches (millimeters) unless otherwise noted.

S8 Package 8-Lead Plastic Small Outline (Narrow 0.150) (LTC DWG # 05-08-1610)



*DIMENSION DOES NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE

**DIMENSION DOES NOT INCLUDE INTERLEAD FLASH. INTERLEAD FLASH SHALL NOT EXCEED 0.010" (0.254mm) PER SIDE

S08 0996

RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT [®] 1178/LT1179	Dual/Quad 17 μ A Precision Single Supply Op Amps	70 μ V Max V_{OS} , 5nA Max I_{BIAS}
LT1351	Single 250 μ A, 3MHz, 200V/ μ s Op Amp with Shutdown	C-Load [™] Op Amp Stable Driving Any Capacitive Load
LT1352/LT1353	Dual/Quad 250 μ A, 3MHz, 200V/ μ s Op Amps	C-Load Op Amps Stable Driving Any Capacitive Load
LTC1440/LTC1540	Micropower Comparator with 1% Reference	1.182V \pm 1% Reference, \pm 10mV (Max) Input Offset
LTC1441/LTC1442	Micropower Dual Comparator with 1% Reference	1.182V \pm 1% Reference (LTC1442)
LTC1443/LTC1444/LTC1445	Micropower Quad Comparator with 1% Reference	LTC1443 Has 1.182V Reference, LTC1444/LTC1445 Have 1.221V Reference and Adjustable Hysteresis
LTC1474	Low Quiescent Current High Efficiency Step-Down Switching Regulator	10 μ A Standby Current, 92% Efficiency, Space Saving 8-Pin MSOP Package
LT1495	1.5 μ A Max, Dual Precision Rail-to-Rail Input and Output Op Amp	375 μ V Max V_{OS} , 250pA I_{BIAS} , 25pA I_{OS}
LT1521	300mA Low Dropout Regulator with Micropower Quiescent Current and Shutdown	0.5V Dropout Voltage, 12 μ A Quiescent Current, Adjustable Output 3V, 3.3V and 5V Fixed
LTC1541/LTC1542	Micropower Op Amp, Comparator and Reference	1.200V \pm 0.8% Reference (LTC1541) Op Amp Outputs Stable with 1000pF Load
LT1634	Micropower Precision Shunt Voltage Reference	1.25V Output, 10 μ A Operating Current, 0.1% Initial Accuracy 10ppm/ $^{\circ}$ C Max Drift

C-Load is a trademark of Linear Technology Corporation.